



09/545040
10/760,108

CFC

Docket No.: S0193.0012
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Letters Patent of:
Juergen Freiwald et al.

Patent No.: 7,062,632

Issued: June 13, 2006

For: METHOD FOR CONTROLLING A
CENTRAL PROCESSING UNIT FOR
ADDRESSING IN RELATION TO A
MEMORY AND CONTROLLER

**REQUEST FOR CERTIFICATE OF CORRECTION
PURSUANT TO 37 CFR 1.322**

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**Certificate
JUL 27 2006
of Correction**

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted a typographical error which should be corrected.

In Claim 1, column 22, line 13, "conimand" should read --command--.

The error was not in the application as filed or amended by Applicant; accordingly no fee is required.

JUL 28 2006

Transmitted herewith is a proposed Certificate of Correction effecting such amendment. Patentees respectfully solicit the granting of the requested Certificate of Correction.

Dated: July 14, 2006

Respectfully submitted,

By *Laura C. Brutman*

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JUL 28 2006

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

Page 1 of 1

PATENT NO. : 7,062,632
APPLICATION NO. : 10/760,108
ISSUE DATE : June 13, 2006
INVENTORS : Juergen Freiwald et al.

It is certified that an error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 1:

Column 22, line 13, "conimand" should read --command--.

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1

JUL 28 2006



What is claimed is:

1. A method for controlling a central processing unit for
addressing in relation to a memory, wherein a set of
5 operation code identifiers including at least one special
operation code identifier is associated to the central
processing unit, wherein the central processing unit is
arranged to address a first memory area having a first
memory size of the memory, wherein the method comprises the
10 following steps:

monitoring a data traffic from the memory to the central
processing unit by supporting means coupled to the central
processing unit;

15 in the case in which the data traffic from the memory to
the central processing unit includes the special operation
code identifier,

20 forming a new address by the supporting means, wherein
the new address is defined in a second memory area
having a second memory size of the memory, wherein the
second memory size is larger than the first memory
size;

25 providing a predetermined operation code identifier to
which a jump command from a command set of the central
processing unit is assigned, to the central processing
unit by the supporting means, wherein the predetermined
30 operation code identifier has a destination address in
relation to the first memory area; and

managing a code descriptor by the supporting means,
yielding, together with the destination address, the
35 new address.

2. A controller having:

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